



IEEE Huntsville Section Meeting



Speaker: Robert C. Persons

Subject: **VME Trade Association (VITA) PCI Mezzanine Modules, PMC, Standards Update**

Date: Tuesday July 29th, 2003

Time: 6:30 – 8:30pm

Place: Hilton Huntsville
401 Williams Ave.
Huntsville, AL
256-533-1400

Reservations: Please contact Sonya Hutchinson at 544-3312, if you plan to attend. You may also email her at: Sonya.L.Hutchinson@nasa.gov.

Biographical Sketch: Robert C. Persons, Principal Staff Systems Engineer with Motorola Computer Group. Rob has been a Field Applications Engineer with Motorola for 14 years and focuses on Military and Aerospace accounts. He participates in several of the VITA Standards bodies focusing on next generation VMEbus and PMC technologies. Rob lives in Central Florida and has a BS in Computer Science from the University of Central Florida.

Abstract: VITA standards used in COTS modules sets the stage for configurable and scalable architecture used on VME, CPCI, or customer form factors. Today there are many suppliers and options existing for I/O lines, processor modules, telecom TDM interconnects, high-speed data transport, and much more. This presentation is to explain current standards, as well as how PMCs can be used for faster development, earlier product release, optimization of internal design talent, and improvement in competitive edge. The topics covered include:

PMC Current Standards:
Vita 32 PPMC; Processor PMC
Vita 36 PMC I/O Modular Standard
Vita 39 PCI-X on PMC
PICMG 2.15 PTMC (Mention DSP PTMC)

PMC Future Standards:
VITA 42 XMC- Switched Mezzanine Card

History of PMCs (IEEE 1386, 1386.1)

Notes: You do not have to be an IEEE member to attend. All guests are welcome. Light Refreshments will be served.



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